



# Morphological studies of Si nanowires effect on the photovoltaics

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Silicon nanowires (SiNWs) were produced by an electroless method on FZ-Si (100) wafer, in HF/AgNO<sub>3</sub> solution. The influence of etching time and temperature on SiNWs morphology were studied using FESEM images. Optical properties were also investigated by optical absorption spectroscopy and low-temperature photoluminescence at 4.2 K. Considering their role as active regions, photo-voltaic properties of SiNWs solar cells were studied for their different lengths. Photovoltaic measurements were taken in 1 sun condition under AM 1.5 illumination supplied by a solar simulator. Measurements indicated a reduction in efficiency as SiNWs length increased, which might be attributed to increased dangling states on nanowires surfaces.

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**Keywords:** Si; PV; Nanowire.

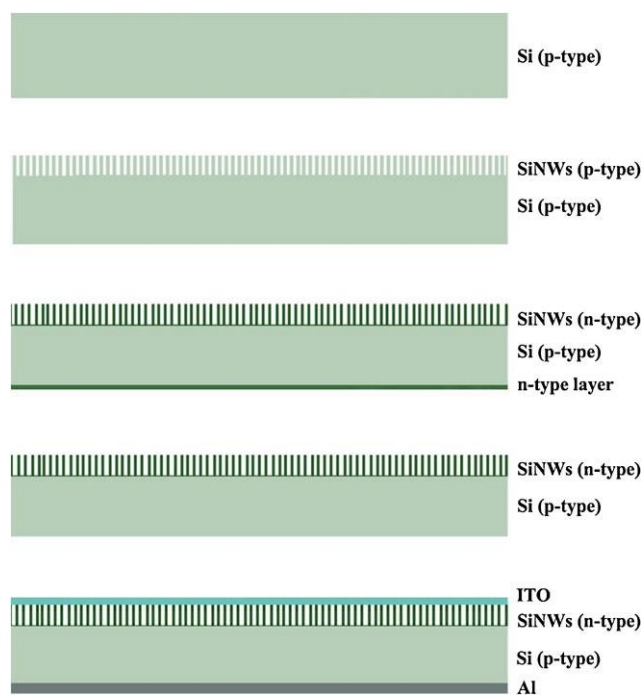
## 1. INTRODUCTION

Considering the ever-increasing energy consumption trends worldwide, scientists are continuously looking for clean and renewable sources of energy such as capturing solar energy by solar cells. Hence, fabrication of solar cells made from one-dimensional nanostructures, has attracted a great deal of interest amongst researchers as cost-effective and highly efficient devices. Their physical properties, however, depend on their nanoscale dimensions, and as such, they provide good application potential in third-generation solar cells for which researchers investigate their electrical and optical properties as their dimension's changes [1–11]. SiNWs arrays on silicon substrate indicate high optical absorption because of strong light trapping by multiple scattering of the incident light among SiNWs and the optical antenna effect [12–15]. This causes very low reflectance

and eliminates antireflective coating step required in the manufacturing process, thereby reducing the production cost of solar cell. Amongst different methods reported previously for fabricating SiNWs, in this study electroless method is employed as a simple and low-cost technique for large scale production of SiNWs on silicon substrate, where the desired growth direction and doping level of SiNWs can be achieved by proper selection of the silicon substrate. Then the effect of reaction time and temperature on morphology and length of SiNWs are studied and finally the photovoltaic properties of the fabricated SiNWs-based solar cells are examined.

## 2. EXPERIMENTAL

In this work, P-type monocrystalline FZ-Si (100) with resistivity around  $1.5\text{--}4\ \Omega\text{ cm}$  from Siltronic was used as raw wafer. The silicon samples were cut into  $1 \times 1\ \text{cm}^2$  and cleaned by immersing them into acetone, ethanol and HF (5 M) as well as de-ionized (DI) water for 10 min each, before etching in a sealed Teflon vessel using a HF (5 M) and  $\text{AgNO}_3$  (0.02 M) solution [15–17]. To investigate the effect of reaction time, experiments were conducted between 10 min to 70 min. Having selected the reaction time of 60 min, the effect of temperature was investigated by tests at 40, 45, 50, 55, 60 and 65 °C. SiNWs growth was studied using a FESEM (Hitachi S-4160) images.



**Figure 1** Schematic view of the fabrication process of SiNWs solar cell.

Optical absorption spectroscopy was carried out within the wavelength range 200–800 nm using a Cary 500 spectrophotometer. The photoluminescence (PL) measurements were carried out using liquid helium-bath cryostats with glass windows. Band-to-band optical transition were provided using a 405 nm line of a 180-mV solid state laser as the excitation source. The monochromator was a 60 cm single grating with 600 grooves/mm. The PL signals were detected by an InGaAs p-i-n detector (Hamamatsu, Japan) and signal amplification was based on lock-in technique.

Figure 1 illustrates the schematic view of fabrication process of SiNWs solar cells in this work. Solar cells were fabricated using the common tube diffusion technique performed by  $\text{POCl}_3$  at 900 °C for 120 min in nitrogen atmosphere. Therefore, P from  $\text{POCl}_3$  diffuses into the P-type wafer (Fig. 1b) upon entry to the furnace, thereby changing it to n-type wafer where for its high density these are

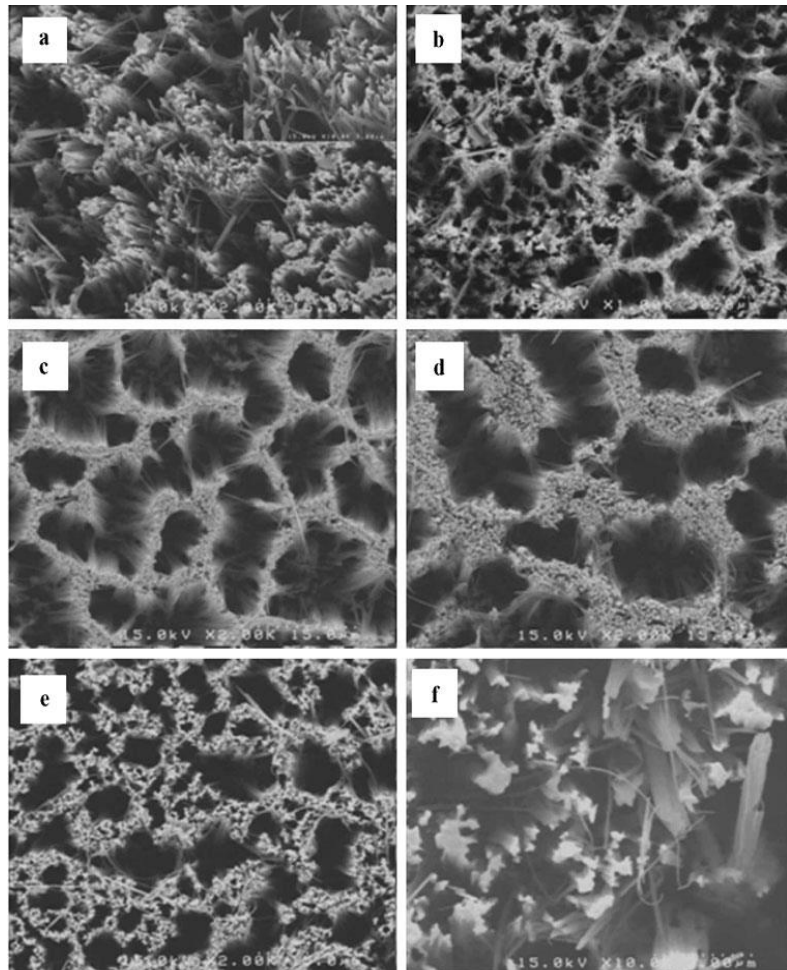
referred to as emitter layers. However, since the rear emitter layer was already an n-type, the post-diffusion wafer becomes an n-p-n type (Fig. 1c). The n-type rear emitter layer was then removed from the back surface of diffused wafer by HF, H<sub>2</sub>SO<sub>4</sub>, HNO<sub>3</sub>, and H<sub>2</sub>O solution, making the final wafer an n-p type wafer (Fig. 1d). 100 nm indium tin-oxide (ITO) and 100 nm aluminum were deposited on silicon surfaces by sputtering and thermal evaporation methods as front and rear electrodes, respectively (Fig. 1e). Finally, the photovoltaic measurements were accomplished by solar simulator (Luzchem) and IVIUMSTAT (IVIUM) under AM1.5 condition.

### 3. RESULTS AND DISCUSSION

Temperature is an important factor in electroless process, controlling the nucleation rate of silver nanoclusters on silicon wafer, and hence its role had to be studied carefully in this research. Nucleation of silver nanoclusters is an important step in the electroless technique and determines the extent of surface etching and density of nanowires on substrate. Figure 2, illustrate the FESEM images of SiNWs formation at different temperatures and 60 min reaction time. As can be seen, many belt-like connections have been formed within the Si nanostructures at 40 °C, (see Fig. 2a and the inset). At this temperature, silver nucleation on silicon wafer does not seem to be substantial [15–17]. Hence, pores provided by Ag nanoparticles in silicon wafer do not meet each other, causing in effect these belt-like attached nanowires. A closer examination of Fig. 2 indicates that increasing temperature up to 55 °C, has improved nucleation; single SiNWs are formed on substrate around 50 °C and 55 °C, but with further rise in temperature, nucleation seems to have decreased and belt-like connections amongst SiNWs are observed again at 65 °C. Therefore, 55 °C was considered as the optimum temperature for SiNWs fabrication at 60 min reaction time in subsequent experiments. To ensure the correctness of the 60 min reaction time, a set of experiments were carried out at 55 °C production temperature and 10 to 70 min reaction times and Figs. 3–4 illustrate top and cross-section view FESEM images of SiNWs nanostructures, respectively. Nucleation of silver nanoclusters depends strongly on AgNO<sub>3</sub> concentration and temperature. As can be seen from Fig. 3, the concentration of SiNWs on silicon surface does not seem to have changed significantly at different reaction times. For clarity, different scales have been employed in Fig. 4. It seems that during redox reaction, silver atoms diffuse into Ag nanoparticles, and these nanoparticles sink into the Si wafer, sinking more and more into the silicon substrate [15–17]. Ag behavior as catalyst leads to formation of a SiNWs layer with specific length on the silicon surface, according to the following re-actions:

At this stage, SiNWs reflection of light for different incident wavelengths had to be investigated to verify the potential absorption for solar cell application.

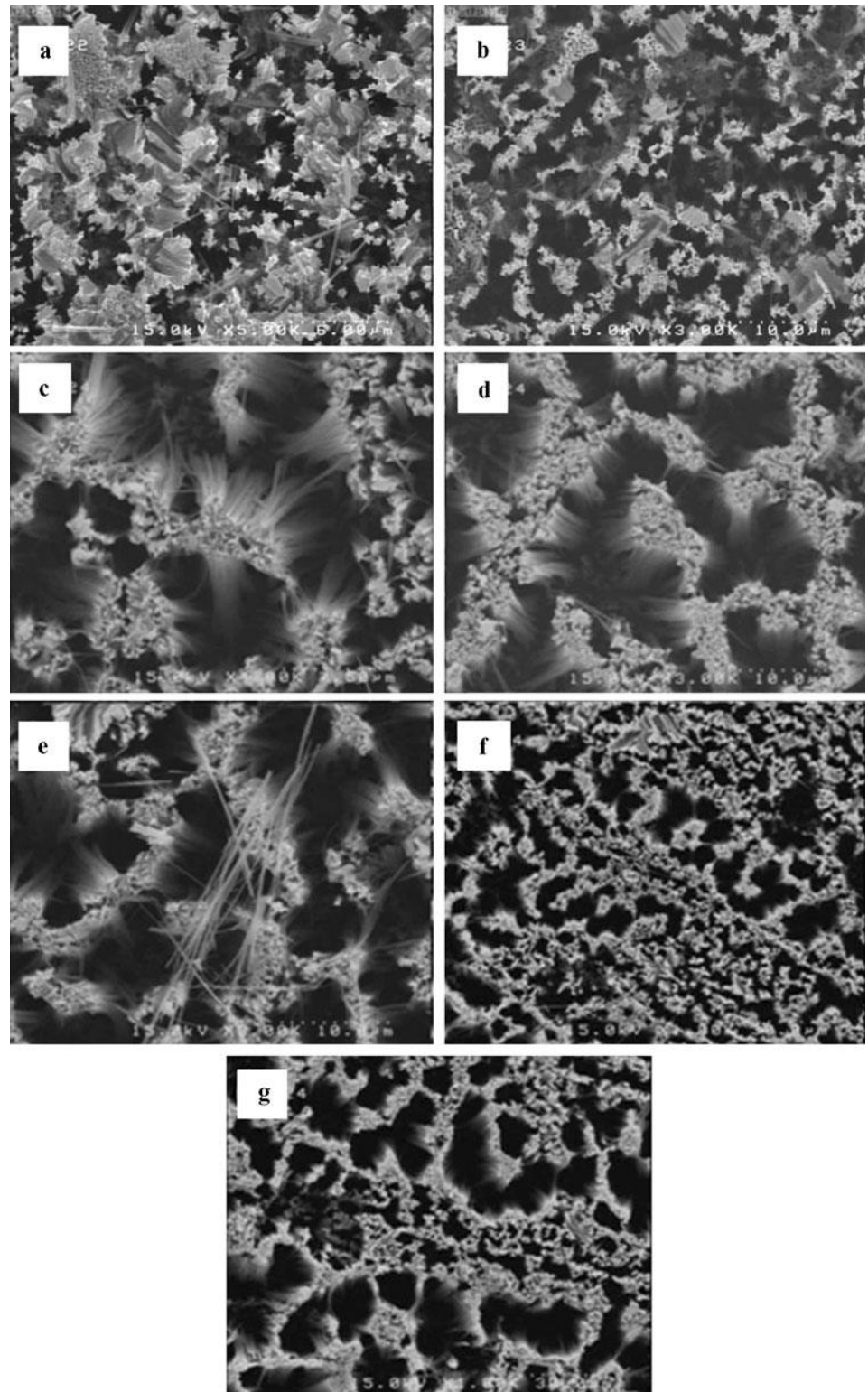
**Figure 2** FESEM images of SiNWs produced by electroless method at 60 min reaction time and different temperatures of (a) 40 °C, (b) 45 °C, (c) 50 °C, (d) 55 °C, (e) 60 °C, (f) 65 °C



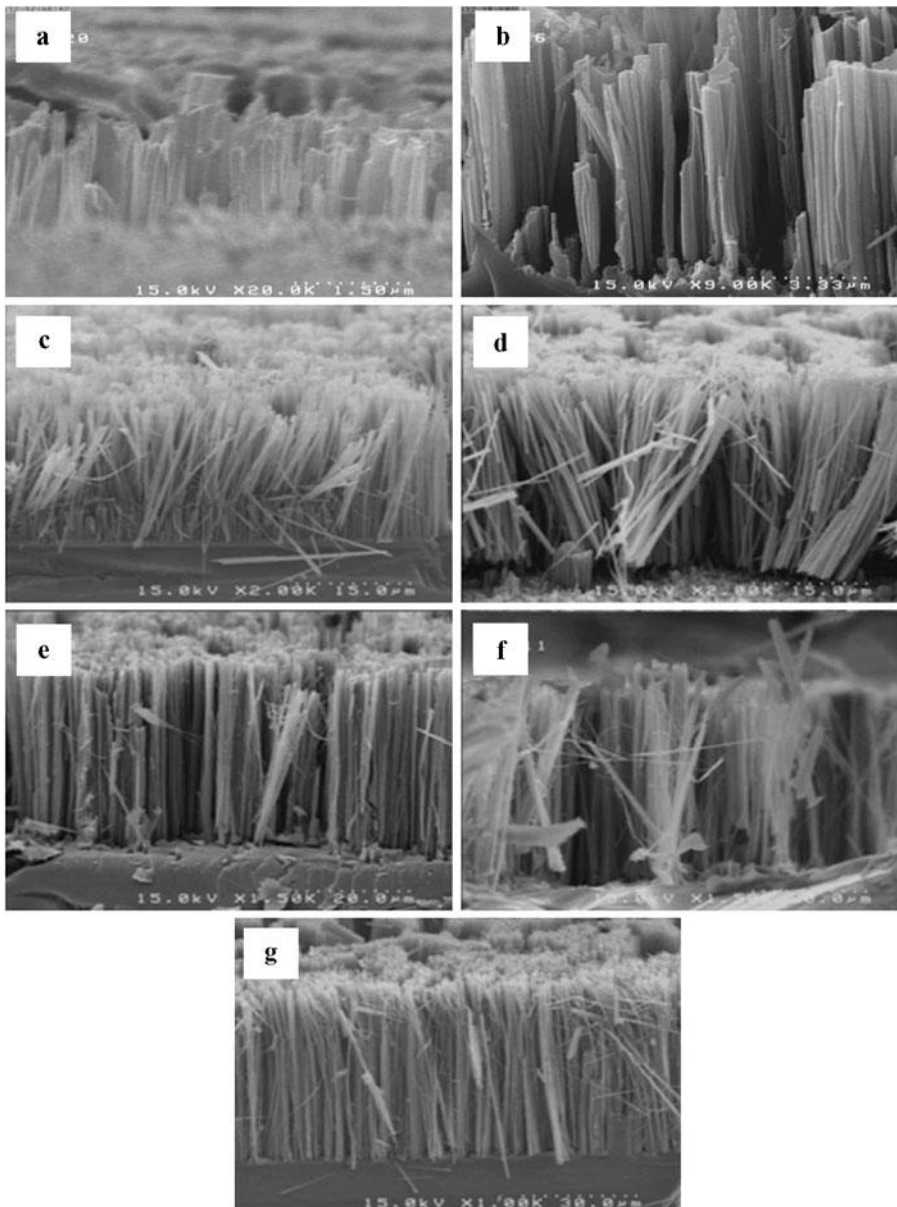
According to this figure, SiNWs have lower reflection than belt-like attached nanowires. Shorter reaction times (i.e. less than 10 min), yielded unetched parts on silicon wafers (Figs. 3a and 3b) and shorter nanowires (Figs. 4a and 4b), leading to lower antireflection properties. Compared to a polished silicon wafer which has reflection ranging approximately from 90 to 40 % in these light wavelengths, clearly a significant reduction in reflection (from 0.25 to 2 %) is experienced for all SiNWs samples tested, confirming the antireflection properties of the SiNWs for this application. However, it is worth noting that increasing etching time and consequently SiNWs length reduces the reflection, be it to a small degree compared to the polished silicon wafer. This behavior could be explained by increased multiple scattering of photons within the longer SiNWs. It should be noted that the reflectivity of the polished wafer in our results, is slightly higher than what is calculated in the literature [18]. This could well be contributed to our experimental errors. As far as PL measurement is concerned, a test was carried out on a sample of 18  $\mu\text{m}$  SiNWs on silicon wafer. The PL spectrum of SiNWs on Si substrate. In the near-band-gap region (1.0–1.2 eV) clearly several somewhat narrow lines are seen, with one intense line at 1.09 eV. These lines correspond to the intrinsic excitonic emission from Si substrate

( $FE^{TA}$ ,  $FE^{T0}$  and so on) [19]. The broad emission band around energy 1.17 eV is most likely due to electron-hole recombination on *SiNWs*. To investigate the photovoltaic properties of *SiNWs*, seven samples, each with surface area 1 cm<sup>2</sup> and different lengths of 1.5, 8, 18, 28, 37, 44 and 51 μm were produced. As mentioned above, by decreasing the reaction time, shorter nanowires are obtained.

**Figure 3** FESEM images (top view) of *SiNWs* produced by electroless method for different reaction times at 55 °C and operating temperatures of (a) 10 min, (b) 20 min, (c) 30 min, (d) 40 min, (e) 50 min, (f) 60 min, and (g) 70 min



However, these nanowires are heterogeneously distributed on the surface having different forms of nanostructures and lower antireflection properties compared with longer nanowires. It is worth noting that shorter SiNWs than  $1.5 \mu\text{m}$  resulted in unacceptable heterogeneity and uniformity in shape and as such  $1.5 \mu\text{m}$  was selected as the minimum length in this work. In addition, considering the characteristics of electroless method, and based on our experience here, the diameter and shape of the produced SiNWs cannot be controlled adequately using this fabrication method.



**Figure 4** FESEM images (cross-section view) of SiNWs produced by electroless method for different reaction times at 55 °C.

The resulting decreased reflection, and increased recombination rate (reduced  $J_{sc}$  and  $V_{oc}$ ) call for procedures such as passivation to reduce the recombination rate and this too should be investigated in further studies to improve the photovoltaic response.

#### 4. CONCLUSIONS

In this work, having considered SiNWs growth on FZ-silicon wafer by electroless technique, an optimum temperature of 55 °C for etching process was determined. SiNWs length was found to depend strongly on the reaction time, while their concentration does not seem to change significantly with reaction time. Optical absorption spectroscopy confirms the antireflective potentials of SiNWs, which is enhanced gradually by increase in nanowires length. Low-temperature PL at 4.2 K indicated the band energy of almost 1.17 eV related to SiNWs. A decreasing trend in efficiency of solar cells was experienced with longer SiNWs. This behavior is thought to be associated with increased dangling states and defects on SiNWs surfaces highlighted by the increased length of SiNWs. In applying SiNWs, the decreased reflection, and increased recombination rate (reduced  $J_{sc}$  and  $V_{oc}$ ) not only call for an optimum length of SiNWs, but also passivation technique, to improve the photovoltaic response.

#### References

- [1] Y. Cui, Q. Wei, H. Park, C.M. Lieber, *Science* 293 (2001) 1289
- [2] M. Marsen, K. Sattler, *Phys. Rev. B* 60 (1999) 11593
- [3] Q. Li, S.M. Koo, M.D. Edelstein, J.S. Suehle, C.A. Richter, *Nanotechnology* 18 2 (2007) 31520
- [4] F. Patolsky, G. Zheng, C.M. Lieber, *Nat. Protoc.* 1 (2006) 1711
- [5] M. Hernandez-Velez, *Thin Solid Films* 495 (2006) 51
- [6] P. Chaudhari, H. Shim, B.A. Wacaser, M.C. Reuter, C. Murray, K.B. Reuter, J. Jordan-Sweet, F.M. Ross, S. Guha, *Thin Solid Films* 518 (2010) 5368
- [7] Z. Li, B. Rajendran, T.I. Kamins, X. Li, Y. Chen, R.S. Williams, *Appl. Phys. A, Mater. Sci. Process* 80 (2005) 257
- [8] T. Duńvki, V. Saryviak, *Exp. Theo. NANOTECHNOLOGY* 3 (2019) 269
- [9] N. Fukata, T. Oshima, N. Okada, T. Kizuka, T. Tsurui, S. Ito, K. Murakami, *Physica B* (2006)
- [10] Y.H. Tang, Y.F. Zhang, N. Wang, C.S. Lee, X.D. Han, I. Bello, S.T. Lee, *J. Appl. Phys.* 85, 7981 (1999) 376
- [11] Dalila MEKAM, Dalila MESRI, Habib. Rozale, *Exp. Theo. NANOTECHNOLOGY* 3 (2019) 281
- [12] Th. Stelzner, M. Pietsch, G. Andrä, F. Falk, E. Ose, S. hris iansen, *Nanotechnology* 19 (2008) 295203
- [13] L. Hu, G. Chen, *Nano Lett.* 7(11) (2007) 3249
- [14] K. Peng, Y. Xu, Y. Wu, Y. Yan, S.T. Lee, J. Zhu, *Small* 1 (2005) 062
- [15] J.Y. Jung, Z. Guo, S.W. Jee, H.D. Um, K.T. Park, J.H. Lee, *pt. Express* 18 (2010) 286
- [16] G. Kalita, S. Adhikari, H.R. Aryal, R. Afre, T. Soga, M. Sharon, W. Koichi, M. Umeno, *J. Phys. D, Appl. Phys* 42 (2009) 1
- [17] S.D. Hutagalung, A.S.Y. Tan, R.Y. Tan, Y. Wahab, *Proc. SPIE* 7743 (2010) 1
- [18] S. Ahmad, *Exp. Theo. NANOTECHNOLOGY* 3 (2019) 301

- [19] A.V. Mudryi, A.I. Patuk, I.A. Shakin, A.G. Ulyashin, R. Job, W.R. Fahrner, A. Fedotov, A. Mazanik, N. Drozdov, *Sol. Energy Mater. Sol. Cells* 72 (2002) 503